

S/N TBD
Docket: CS03-048
Group art unit : __ TBD

Date 1/30/04

To: Commissioner of Patents and Trademarks
P.O. Box 1450 Alexandria, VA 22313-1450

Fr: William J. Stoffel Reg. No. 39,390 CUST NO. 30402
PMB 455
1735 Market St - Suite A
Philadelphia, PA 19103

Subject:

Serial No. TBD
Docket cs03-048
File Date: with application
Inventor: Li et al.

**title: A SEMICONDUCTOR DEVICE LAYOUT AND CHANNELING
IMPLANT PROCESS**

INFORMATION DISCLOSURE STATEMENT

Enclosed is Form PTO A820 (also PTO-1449), Information
Disclosure Citation and references.

CERTIFICATE OF MAILING OR EXPRESS MAILING

I hereby certify that this correspondence is being deposited with the
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Signature/Date

William J. Stoffel 1/30/04
William J. Stoffel Reg. No. 39,390
Customer number 30402

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The following Patents and/or Publication are submitted to comply with the duty to disclose under CFR 1.97-1.99 and 37 CFR 1.56.

US 5,970,300 (Buynoski) shows a I/I process.

US 5,729,045 (Buynoski) shows a I/I process for S/D.

US 6,566,204 (Wang, et al.) teaches the use of mask shadowing and angled implantation in fabricating asymmetrical field-effect transistors.

US 6,599,804 (Bulucea, et al.) shows a fabrication of field-effect transistor for alleviating short-channel effects.

US 4,728,617 (Woo, et al.) shows a method of fabricating a MOSFET with graded source and drain regions.

Sincerely,



William J. Stoffel

Reg. No. 39,390

Customer number 30,402

INFORMATION DISCLOSURE CITATION <i>(Use several sheets if necessary)</i>	Docket Number (Optional) CS03-048	Application Number
	Applicant(s) Yisuo Li	
	Filing Date	Group Art Unit

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
		US 5,970,300		(Buynoski)			
		US 6,566,204		(Wang, et al.)			
		US 6,599,804		(Bulucea, et al.			
		US 4,728,617		Woo, et al			
		US 5,729,045		Buynoski			

FOREIGN PATENT DOCUMENTS

REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						YES	NO

OTHER DOCUMENTS *(Including Author, Title, Date, Pertinent Pages, Etc.)*

EXAMINER	DATE CONSIDERED
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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.